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APPLICANT: Edmund Cheung *et al.*

APPLICATION NO.: 09/270,312

FILING DATE: March 16, 1999

TITLE: SYSTEM AND METHOD FOR DYNAMIC CLOCK GENERATION

EXAMINER: Melvin C. Marcelo

GROUP ART UNIT: 2663

ATTY. DKT. NO.: 21726-03814

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being sent via facsimile transmission to the U.S. Patent and Trademark Office at facsimile number (703) 872-9314 (Attn.: Examiner Melvin C. Marcelo, Group Art Unit 2663) and addressed to: Commissioner for Patents, Washington, D.C. 20231, on the date printed below:

Dated: January 9, 2003By: Nathan S. Huynh
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AMENDMENT AFTER FINAL REJECTION

SIR:

In response to the Office Action mailed December 2, 2002, please amend the application as follows:

IN THE CLAIMS:

Please cancel claims 1, 3, 6-8, and 14-16, and amend claim 4 as follows.

2. The system of claim 20, wherein the resource is a memory controller for controlling access to a memory using a memory clock and a clock enable signal and the system further comprises:

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